Exhibit 4

STMIcroelectronics – Microprocessor Unit (MPU) Device (see Product List at end for models)

Infringement of the '242 patent

Claim 1	Evidence
1. A method of processing imaging signals, the method comprising:	The STMIcroelectronics MPU device performs a method of processing imaging signals. For example, the STMIcroelectronics MPU device has a camera interface (e.g. that supports 8-bit to 14-bit input signals) which enables an image sensor to be coupled to the MPU device. The image sensor includes an imaging array and a clock generator. The STMIcroelectronics MPU device includes an image processor. The image processor processes imaging signals that are received from the image sensor via the camera interface.
receiving image data from an imaging array;	The STMIcroelectronics MPU device receives image data from an imaging array. For example, the image sensor includes an imaging array. The imaging array produces image data when exposed to an image. The camera interface of the STMIcroelectronics MPU device receives the image data from the imaging array.
storing the image data in a FIFO memory;	The STMIcroelectronics MPU device stores the image data in a FIFO memory. For example, the camera interface includes a FIFO memory for storing image data. The image data received from the imaging array is stored in the FIFO memory by the camera interface.
updating a FIFO counter to maintain a count of the image data in the FIFO memory in response to memory reads and writes;	The STMIcroelectronics MPU device updates a FIFO counter to maintain a count of the image data in the FIFO memory in response to memory reads and writes; For example, the camera interface includes a FIFO counter to maintain a count of the image data, or "fill level", that is stored in the FIFO memory. When a unit of image data is written to the FIFO memory, the count of the FIFO counter is incremented. When a unit of image data is read from the FIFO memory, the count of the FIFO

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	counter is decremented.
comparing the count of the FIFO counter with a FIFO limit;	The STMIcroelectronics MPU device compares the count of the FIFO counter with a FIFO limit. For example, the camera interface includes a FIFO limit which it compares to the FIFO count to determine if the amount of image data in the FIFO memory is at a "fill level" that will require the camera interface to take an action.
generating an interrupt signal to request a processor to transfer image data from the FIFO memory in response to an interrupt enable signal being valid and the count of the FIFO counter having a predetermined relationship to the FIFO limit; and	The STMIcroelectronics MPU device generates an interrupt signal to request a processor to transfer image data from the FIFO memory in response to an interrupt enable signal being valid and the count of the FIFO counter having a predetermined relationship to the FIFO limit. For example, the camera interface includes circuitry for performing operations to transmit image data to the image processor. The servicing of interrupts by the image processor can be enabled or disabled. When the servicing of interrupts from the camera interface is enabled and the count of the FIFO counter has a predetermined relationship to the FIFO limit, the camera interface generates an interrupt signal. The interrupt signal represents a request for the image processor to transfer image data from the FIFO memory.
transferring image data from the FIFO memory to the processor in response to the interrupt signal.	The STMIcroelectronics MPU device transfers image data from the FIFO memory to the processor in response to the interrupt signal. For example, when the image processor receives the interrupt signal, the image processor transfers the image data from the FIFO memory to the image processor for processing.

STMIcroelectronics – Microprocessor Unit (MPU) Device (see Product List at end for models) Infringement of the '242 patent	
Claim 8	**Evidence**
8. A method of	The STMIcroelectronics MPU device performs a

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processing imaging signals, the method comprising:	method of processing imaging signals. For example, the STMIcroelectronics MPU device has a camera interface (e.g. that supports 8-bit to 14-bit input signals) which enables an image sensor to be coupled to the MPU device. The image sensor includes an imaging array and a clock generator. The STMIcroelectronics MPU device includes an image processor. The image processor processes imaging signals that are received from the image sensor via the camera interface.
receiving image data from an imaging array;	The STMIcroelectronics MPU device receives image data from an imaging array. For example, the image sensor includes an imaging array. The imaging array produces image data when exposed to an image. The camera interface of the STMIcroelectronics MPU device receives the image data from the imaging array.
storing the image data in a FIFO memory;	The STMIcroelectronics MPU device stores the image data in a FIFO memory. For example, the camera interface includes a FIFO memory for storing image data. The image data received from the imaging array is stored in the FIFO memory by the camera interface.
updating a FIFO counter to maintain a count of the image data in the FIFO memory in response to memory reads and writes;	The STMIcroelectronics MPU device updates a FIFO counter to maintain a count of the image data in the FIFO memory in response to memory reads and writes; For example, the camera interface includes a FIFO counter to maintain a count of the image data, or "fill level", that is stored in the FIFO memory. When a unit of image data is written to the FIFO memory, the count of the FIFO counter is incremented. When a unit of image data is read from the FIFO memory, the count of the FIFO counter is decremented.
comparing the count of the FIFO counter with a FIFO limit;	The STMIcroelectronics MPU device compares the count of the FIFO counter with a FIFO limit. For example, the camera interface includes a FIFO limit which it compares to the FIFO count to determine if the amount of image data in the FIFO

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	memory is at a "fill level" that will require the camera interface to take an action.
generating, in response to the count of the FIFO counter having a predetermined relationship to the FIFO limit, a bus request signal to request a bus arbitration unit to grant access to an output bus; and	The STMIcroelectronics MPU device generates, in response to the count of the FIFO counter having a predetermined relationship to the FIFO limit, a bus request signal to request a bus arbitration unit to grant access to an output bus. For example, the camera interface includes a bus arbitration unit and an output bus to which the image processor is connected. When the count of the FIFO counter has a predetermined relationship to the FIFO limit, the camera interface generates a bus request signal. The bus request signal represents a request for the bus arbitration unit to grant the camera interface access to the output bus.
transferring image data from the FIFO memory to the output bus in response to receiving a grant signal from the bus arbitration unit.	The STMIcroelectronics MPU device transfers image data from the FIFO memory to the output bus in response to receiving a grant signal from the bus arbitration unit. For example, after the bus arbitration unit receives the bus request signal it generates a grant signal that gives the camera interface access to the output bus. Upon receiving the grant signal, the image data is transferred from the FIFO memory to the output bus for processing by the image processor.

Product List

STM32MP1 Series MPUs:
STM32MP151A, STM32MP151C, STM32MP151D, STM32MP151F
STM32MP153A, STM32MP153C, STM32MP153D, STM32MP153F
STM32MP157A, STM32MP157C, STM32MP157D, STM32MP157F

References

[1] STM32MP151A
https://www.st.com/resource/en/datasheet/stm32mp151a.pdf

[2] STM32MP151C
https://www.st.com/resource/en/datasheet/stm32mp151c.pdf

[3] STM32MP151D

https://www.st.com/resource/en/datasheet/stm32mp151d.pdf

[4] STM32MP151F
https://www.st.com/resource/en/datasheet/stm32mp151f.pdf

[5] STM32MP153A
https://www.st.com/resource/en/datasheet/stm32mp153a.pdf

[6] STM32MP153C
https://www.st.com/resource/en/datasheet/stm32mp153c.pdf

[7] STM32MP153D
https://www.st.com/resource/en/datasheet/stm32mp153d.pdf

[8] STM32MP153F
https://www.st.com/resource/en/datasheet/stm32mp153f.pdf

[9] STM32MP157A
https://www.st.com/resource/en/datasheet/stm32mp157a.pdf

[10] STM32MP157C
https://www.st.com/resource/en/datasheet/stm32mp157c.pdf

[11] STM32MP157D
https://www.st.com/resource/en/datasheet/stm32mp157d.pdf

[12] STM32MP157F
https://www.st.com/resource/en/datasheet/stm32mp157f.pdf